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What is claimed is:

- 1. A method for making a protected MEMS structure, comprising the steps of:
- (a) preparing a MEMS wafer having a plurality of MEMS structure sites thereon;
- (b) mounting, upon the MEMS wafer, a spacer layer, the spacer layer being perforated in areas corresponding to locations of the MEMS structure sites on the MEMS wafer; and
- (c) mounting, upon the spacer layer, a wafer cap to produce a laminated MEMS wafer, the spacer layer having a height to prevent electrostatically induced damage to the MEMS wafer.
- 2. The method as claimed in claim 1, wherein the spacer layer comprises a tape having adhesive on two sides and a flexible film.
- 3. The method as claimed in claim 1, wherein the spacer layer comprises a flexible film with an adhesive medium on one side.
- 4. The method as claimed in claim 2, wherein the flexible film is transmissive to UV radiation.
- 5. The method as claimed in claim 3, wherein the flexible film is transmissive to UV radiation.
 - 6. The method as claimed in claim 1, wherein the wafer cap is a cover tape.
- 7. The method as claimed in claim 1, wherein the cover tape comprises a static dissipative material.
 - 8. The method as claimed in claim 2, wherein the wafer cap is a cover tape.
- 9. The method as claimed in claim 3, wherein the wafer cap is a cover tape.
- 10. The method as claimed in claim 4, wherein the wafer cap is a cover tape.
- 1 11. The method as claimed in claim 5, wherein the wafer cap is a cover tape.
 - 12. The method as claimed in 1, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.

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- 13. The method as claimed in 2, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 14. The method as claimed in 3, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 15. The method as claimed in 4, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 16. The method as claimed in 5, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 17. The method as claimed in 6, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 18. The method as claimed in 7, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 19. The method as claimed in 8, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 20. The method as claimed in 9, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 21. The method as claimed in 10, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
- 22. The method as claimed in 11, wherein the height of the spacer layer prevents the wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
 - 23. The method as claimed in claim 2, wherein the flexible film is about 40 mils thick.
- 24. The method as claimed in claim 1, wherein the spacer layer comprises a plurality of layers of perforated tape, an aggregate of the plurality of layers of perforated tape producing the height to prevent electrostatically induced damage to the MEMS wafer.
- 25. The method as claimed in claim 2, wherein the flexible film and tape are combined and then cut to produce areas corresponding to the MEMS structures on the MEMS wafer.

27. The method as claimed in claim 2, wherein the flexible film and tape are combined and then punched to produce areas corresponding to the MEMS structures on the MEMS wafer.

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- 28. The method as claimed in claim 2, wherein the flexible film and tape are pre-cut to produce areas corresponding to the MEMS structures on the MEMS wafer before being combined.
- 29. The method as claimed in claim 2, wherein the flexible film and tape are prepunched to produce areas corresponding to the MEMS structures on the MEMS wafer before being combined.
- 30. The method as claimed in claim 25, wherein the flexible film and tape are combined using pressure to promote adhesion.
 - 31. A method for making a protected MEMS structure, comprising the steps of:
 - (a) preparing a MEMS wafer having a plurality of MEMS structure sites thereon;
- (b) mounting, upon the MEMS wafer, a spacer layer, the spacer layer being perforated in areas corresponding to locations of the MEMS structure sites on the MEMS wafer; and
- (c) mounting, upon the spacer layer, a wafer cap to produce a laminated MEMS wafer, the spacer layer having a height to prevent damage to the MEMS structures due to the wafer cap coming into physical contact with the MEMS wafer.
- 32. The method as claimed in claim 31, wherein the spacer layer comprises a tape having adhesive on two sides and a flexible film.
- 33. The method as claimed in claim 31, wherein the spacer layer comprises a flexible film with an adhesive medium on one side.
- 34. The method as claimed in claim 32, wherein the flexible film is transmissive to UV radiation.

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- 1 35. The method as claimed in claim 33, wherein the flexible film is transmissive to UV radiation.
 - 36. The method as claimed in claim 31, wherein the wafer cap is a cover tape.
 - 37. The method as claimed in claim 31, wherein the cover tape comprises a static dissipative material.
 - 38. The method as claimed in claim 32, wherein the wafer cap is a cover tape.
 - 39. The method as claimed in claim 33, wherein the wafer cap is a cover tape.
 - 40. The method as claimed in claim 34, wherein the wafer cap is a cover tape.
 - 41. The method as claimed in claim 35, wherein the wafer cap is a cover tape.
 - 42. The method as claimed in claim 32, wherein the flexible film is about 40 mils thick.
 - 43. The method as claimed in claim 31, wherein the spacer layer comprises a plurality of layers of perforated tape, an aggregate of the plurality of layers of perforated tape producing the height to prevent damage to the MEMS structures due to the wafer cap coming into physical contact with the MEMS wafer.
 - 44. The method as claimed in claim 32, wherein the flexible film and tape are combined and then cut to produce areas corresponding to the MEMS structures on the MEMS wafer.
 - 45. The method as claimed in claim 32, wherein the flexible film and tape are combined and then cut by a laser to produce areas corresponding to the MEMS structures on the MEMS wafer.
 - 46. The method as claimed in claim 32, wherein the flexible film and tape are combined and then punched to produce areas corresponding to the MEMS structures on the MEMS wafer.
 - 47. The method as claimed in claim 32, wherein the flexible film and tape are pre-cut to produce areas corresponding to the MEMS structures on the MEMS wafer before being combined.

48. The method as claimed in claim 44, wherein the flexible film and tape are pre-

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dissipative material.

57. The method as claimed in claim 51, wherein the wafer cap is a cover tape.

58. The method as claimed in claim 52, wherein the wafer cap is a cover tape.

59. The method as claimed in claim 53, wherein the wafer cap is a cover tape.

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MEMS wafer being a side opposite of a side having the wafer cap located thereon.

(d) applying a contiguous tape on a backside of the MEMS wafer, the backside of the

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- a spacer layer mounted upon the MEMS wafer, the spacer layer being perforated in areas corresponding to locations of the MEMS structure sites on the MEMS wafer; and a wafer cap mounted upon said spacer layer to produce a laminated MEMS wafer; said spacer layer having a height to prevent electrostatically induced damage to said MEMS wafer.
- 82. The laminated MEMS wafer as claimed in claim 81, wherein said spacer layer comprises a tape having adhesive on two sides and a flexible film.
 - 83. The laminated MEMS wafer as claimed in claim 81, wherein said spacer layer comprises a flexible film with an adhesive medium on one side.
 - 84. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film is transmissive to UV radiation.
 - 85. The laminated MEMS wafer as claimed in claim 83, wherein said flexible film is transmissive to UV radiation.
 - 86. The laminated MEMS wafer as claimed in claim 81, wherein said wafer cap is a cover tape.
 - 87. The laminated MEMS wafer as claimed in 81, wherein the height of said spacer layer prevents said wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
 - 88. The laminated MEMS wafer as claimed in 86, wherein the height of said spacer layer prevents said wafer cap from deflecting in such a manner to come in contact with the MEMS structures.
 - 89. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film is about 40 mils thick.
 - 90. The laminated MEMS wafer as claimed in claim 81, wherein said spacer layer comprises a plurality of layers of perforated tape, an aggregate of the plurality of layers of perforated tape producing the height to prevent electrostatically induced damage to said MEMS wafer.

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- 91. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film and tape are combined and then cut to produce areas corresponding to the MEMS structures on said MEMS wafer.
- 92. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film and tape are combined and then punched to produce areas corresponding to the MEMS structures on said MEMS wafer
 - 93. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film and tape are pre-cut to produce areas corresponding to the MEMS structures on said MEMS wafer before being combined.
 - 94. The laminated MEMS wafer as claimed in claim 82, wherein said flexible film and tape are pre-punched to produce areas corresponding to the MEMS structures on said MEMS wafer before being combined.
 - 95. The laminated MEMS wafer as claimed in claim 91, wherein said flexible film and tape are combined using pressure to promote adhesion.
 - 96. A laminated MEMS wafer, comprising:
 - a MEMS wafer having a plurality of MEMS structure sites located thereon;
 - a spacer layer mounted upon the MEMS wafer, the spacer layer being perforated in areas corresponding to locations of the MEMS structure sites on the MEMS wafer; and
 - a wafer cap mounted upon said spacer layer to produce a laminated MEMS wafer; said spacer layer having a height to prevent damage to the MEMS structures due to said wafer cap coming into physical contact with said MEMS wafer.
 - 97. The laminated MEMS wafer as claimed in claim 96, wherein said spacer layer comprises a tape having adhesive on two sides and a flexible film.
 - 98. The laminated MEMS wafer as claimed in claim 96, wherein said spacer layer comprises a flexible film with an adhesive medium on one side.
 - 99. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film is transmissive to UV radiation.

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- 100. The laminated MEMS wafer as claimed in claim 98, wherein said flexible film is transmissive to UV radiation.
- 101. The laminated MEMS wafer as claimed in claim 96, wherein said wafer cap is a cover tape.
 - 102. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film is about 40 mils thick.
 - 103. The laminated MEMS wafer as claimed in claim 96, wherein said spacer layer comprises a plurality of layers of perforated tape, an aggregate of the plurality of layers of perforated tape producing the height to prevent damage to the MEMS structures due to said wafer cap coming into physical contact with said MEMS wafer.
 - 104. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film and tape are combined and then cut to produce areas corresponding to the MEMS structures on said MEMS wafer.
 - 105. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film and tape are combined and then punched to produce areas corresponding to the MEMS structures on said MEMS wafer.
 - 106. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film and tape are pre-cut to produce areas corresponding to the MEMS structures on said MEMS wafer before being combined.
- 107. The laminated MEMS wafer as claimed in claim 97, wherein said flexible film and tape are pre-punched to produce areas corresponding to the MEMS structures on said MEMS wafer before being combined.
- 108. The laminated MEMS wafer as claimed in claim 104, wherein said flexible film and tape are combined using pressure to promote adhesion.
 - 109. A laminated MEMS wafer, comprising:
- a MEMS wafer having a plurality of MEMS structure sites located thereon;

 a spacer layer mounted upon the MEMS wafer, the spacer layer being perforated in areas corresponding to locations of the MEMS structure sites on the MEMS wafer; and a wafer cap mounted upon said spacer layer to produce a laminated MEMS wafer; said spacer layer having a height to prevent damage to the MEMS structures due to said wafer cap coming into physical contact with said MEMS wafer and to prevent electrostatically induced damage to said MEMS wafer.

- 110. The laminated MEMS wafer as claimed in claim 109, wherein said spacer layer comprises a tape having adhesive on two sides and a flexible film.
- 111. The laminated MEMS wafer as claimed in claim 109, wherein said spacer layer comprises a flexible film with an adhesive medium on one side.
- 112. The laminated MEMS wafer as claimed in claim 110, wherein said flexible film is transmissive to UV radiation.
- 113. The laminated MEMS wafer as claimed in claim 111, wherein said flexible film is transmissive to UV radiation.
- 114. The laminated MEMS wafer as claimed in claim 109, wherein said wafer cap is a cover tape.
- 115. The laminated MEMS wafer as claimed in claim 110, wherein said flexible film is about 40 mils thick.
- 116. The laminated MEMS wafer as claimed in claim 109, wherein said spacer layer comprises a plurality of layers of perforated tape, an aggregate of the plurality of layers of perforated tape producing the height to prevent damage to the MEMS structures due to said wafer cap coming into physical contact with said MEMS wafer and to prevent electrostatically induced damage to said MEMS wafer.
- 117. The laminated MEMS wafer as claimed in claim 110, wherein said flexible film and tape are combined and then cut to produce areas corresponding to the MEMS structures on said MEMS wafer

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dissipative material.

dissipative material.

118. The laminated MEMS wafer as claimed in claim 110, wherein said flexible film and

tape are combined and then punched to produce areas corresponding to the MEMS structures on

127. The method as claimed in claim 50, wherein the spacer layer comprises a static

128. The method as claimed in claim 69, wherein the contiguous tape comprises a static

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- 1 141. The laminated MEMS wafer as claimed in claim 140, wherein said contiguous tape 2 is applied to the backside of said MEMS wafer after the laminated MEMS wafer is sawn.
 - 142. The laminated MEMS wafer as claimed in claim 96, further comprising: a contiguous tape applied to a backside of said MEMS wafer, the backside of said MEMS wafer being a side opposite of a side having said wafer cap located thereon.
 - 143. The laminated MEMS wafer as claimed in claim 142, wherein said contiguous tape is applied to the backside of said MEMS wafer after the laminated MEMS wafer is sawn.
 - 144. The laminated MEMS wafer as claimed in claim 109, further comprising: a contiguous tape applied to a backside of said MEMS wafer, the backside of said MEMS wafer being a side opposite of a side having said wafer cap located thereon.
 - 145. The laminated MEMS wafer as claimed in claim 144, wherein said contiguous tape is applied to the backside of said MEMS wafer after the laminated MEMS wafer is sawn.